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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/659,017	09/11/2003		Paul Lapstun	IP37US	3019	
24011	7590	02/07/2005	·	EXAMINER		
SILVERBI 393 DARLI		ESEARCH PTY LT	GRANT II, JEROME			
BALMAIN.		D1		ART UNIT	PAPER NUMBER	
AUSTRAL	Α			2626		

DATE MAILED: 02/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

e V	Application No. Applicant(s)				
	10/659,017	LAPSTUN ET AL.			
Office Action Summary	Examiner	Art Unit			
	Jerome Grant II	2626			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period volume to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timy within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on					
2a) ☐ This action is <b>FINAL</b> . 2b) ☑ This	action is non-final.				
3) Since this application is in condition for alloward closed in accordance with the practice under E	•				
Disposition of Claims					
4) ☐ Claim(s) 1-10 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-5 is/are rejected. 7) ☐ Claim(s) 6-10 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.				
Application Papers					
9) The specification is objected to by the Examine	r.				
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correct  11) The oath or declaration is objected to by the Ex	-	• •			
Priority under 35 U.S.C. § 119	4.				
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori	s have been received. s have been received in Application ity documents have been receive n (PCT Rule 17.2(a)).	on No d in this National Stage			
Attachment(s)					
Notice of References Cited (PTO-892)	4) Interview Summary				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) B) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 2-1-2005.	Paper No(s)/Mail Da 5)  Notice of Informal Pa 6)  Other:	te atent Application (PTO-152)			

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## **Detailed Action**

1.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rampuria in view of Dvorzsak.

With respect to claim 1, Rampuria teaches an integrated circuit fabricated on a substrate 10. See the housing circuit for a plurality of boards taught at col. 3, lines 63-69. Rampuria teaches the substrate 10 comprising a first interface (12) or printer interface 40 for the purpose of interfacing with a plotter/printer 38; and a general processor (microprocessor 14 or communication controller 20) which is connected to the first interface where the processor runs software beginning at col. 3, lines 63 that controls the interface as well as other components of the substrate 10.

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What is not specifically taught is that the printer 38 is an inkjet printer, although it is inferred.

Dvorzsak teaches integrated circuits 201 and 223 and a first interface 241 for operating a inkjet print head 233 and 237. Dvorzsak teaches a processor 243 for controlling the interface 241.

Since Rampuria and Dvorzsak are both directed toward integrated circuits with interfaces for causing print data to be printed by a printing means, the purpose of printing by using an ink printer would have been contemplated by Rampuria as specifically set forth by Dvorzsak.

It would have been obvious to one of ordinary skill in the art to replace printer/plotter 38 with the inkjet printer 230-237 for the purpose of printing with a inkjet printhead that is placed in a page width direction as performed by Dvorzsak

With respect to claim 2, Rampuria teaches printing pages in a stream mode. See col. 3, lines 23-26.

With respect to claim 3, Rampuria teaches, according to col. 9, lines 10 that a threshold memory is a FIFO with 32 words of memory for printing at least a single page. So that when the data in the FIFO fills up, it is printed out. Hence the threshold is 32 words.

With respect to claim 4, Rampuria teaches a memory threshold of 32 words

With respect to claim 5, Rampuria teches a 6 MB memory which anticipates the user of 3 MB memory, see col. 4, lines 38-43.

2.

## **Claims Objected**

Claims 6-10 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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3.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jerome Grant II whose telephone number is 703-305-4391. The examiner can normally be reached on Mon.-Fri. from 9:00 to 5:0.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly A Williams, can be reached on 703-305-4863. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JEROMÉ GRANT II PRIMARY EXAMINER

J. Grant II